

What is claimed is:

1. A memory device comprising:
  - a counter to count addresses of rows of memory cells;
  - a register to store selected addresses of rows of memory cells; and
  - a compare circuit connected to the counter and the register to compare an address counted by the counter with the selected addresses such that a row of memory cells located at the address counted by the counter is refreshed if the address counted by the counter is within the selected addresses.
  
2. A method of refreshing memory cells of a memory device, the method comprising:
  - generating a count that represents an address of a row of the memory cells;
  - comparing the address represented by the count with a selected address; and
  - refreshing memory cells of a row located at the address counted by the count when the address represented by the count matches the selected address.
  
3. A method of refreshing memory cells of a memory device, the method comprising:
  - storing a plurality of stored data bits that presents selected addresses of rows of the memory cells;
  - generating a plurality of counts, the counts representing addresses of a plurality of rows of memory cells;
  - comparing each of the counts with the stored data bits; and
  - refreshing memory cells of a row located at an address represented by one of the count when the address represented by one of the count matches the selected addresses.